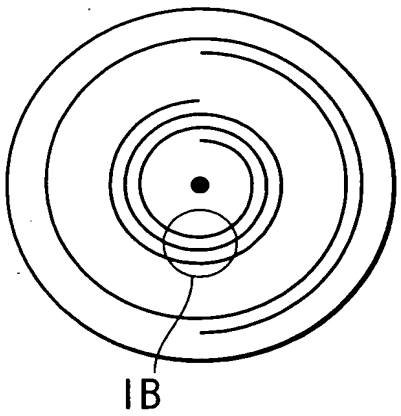

FIG. 1A

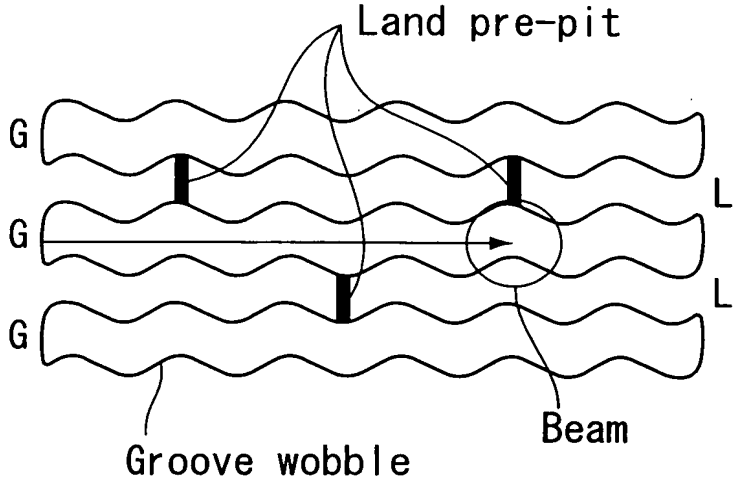

FIG. 1B

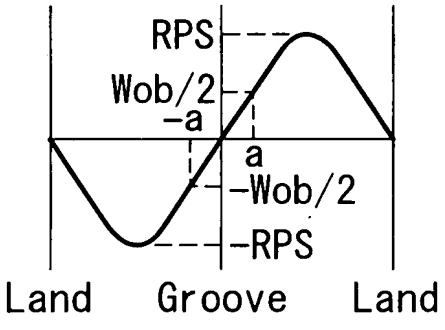

FIG. 2A

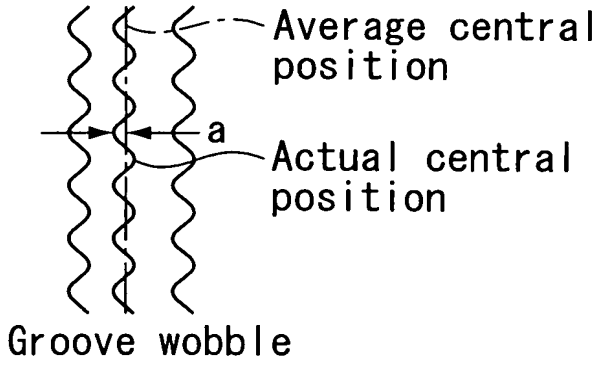

FIG. 2B

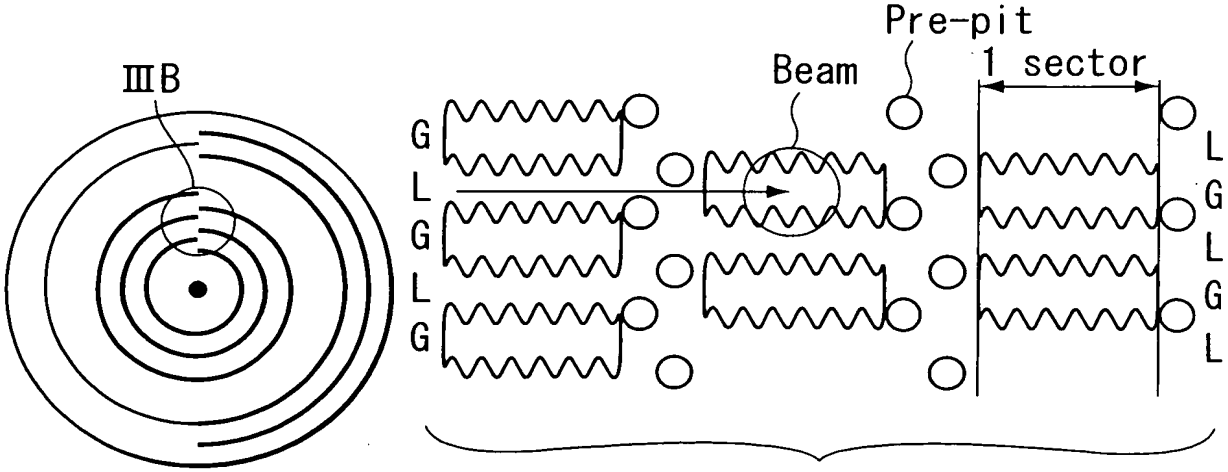

FIG. 3A

FIG. 3B

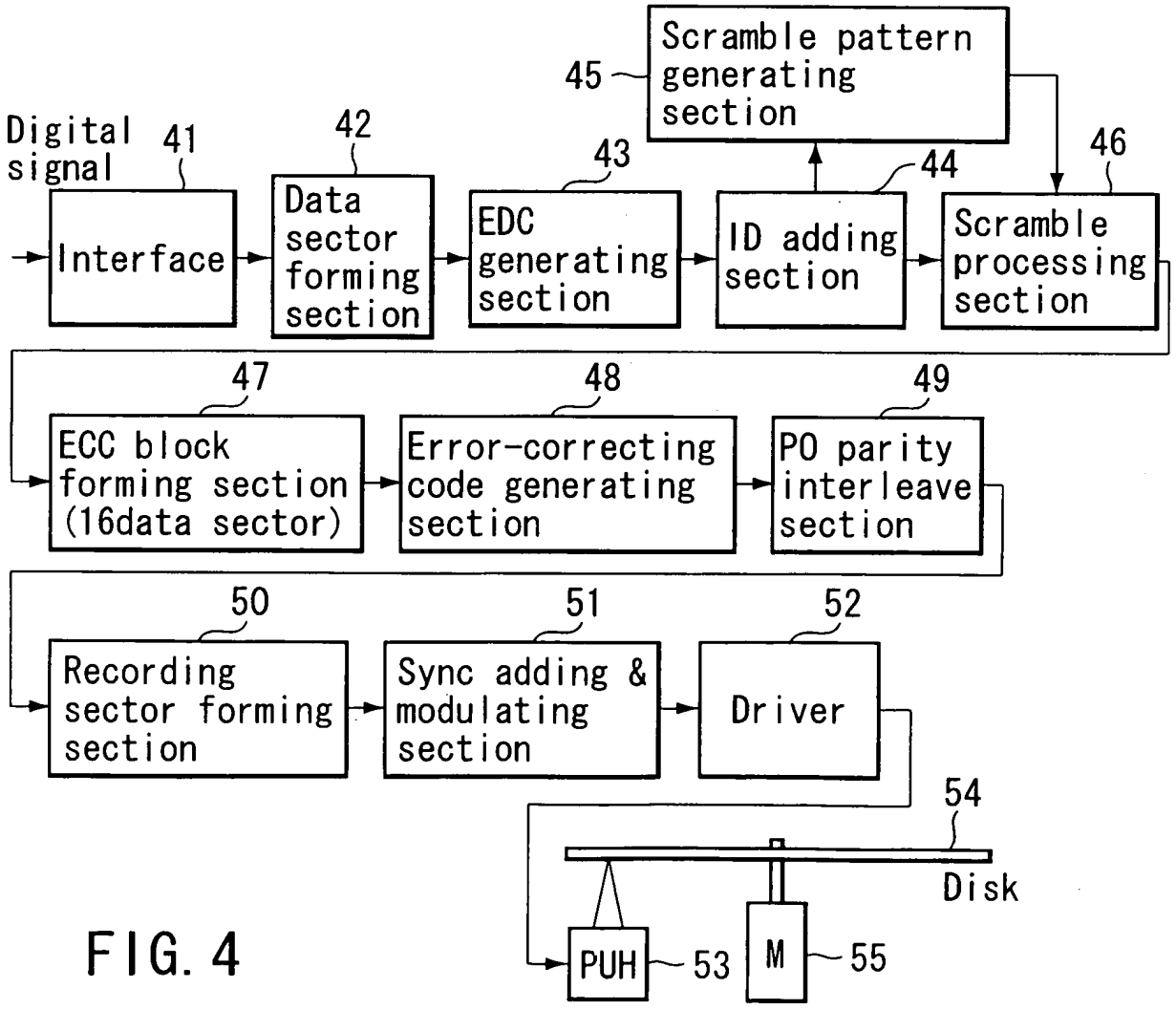

FIG. 4

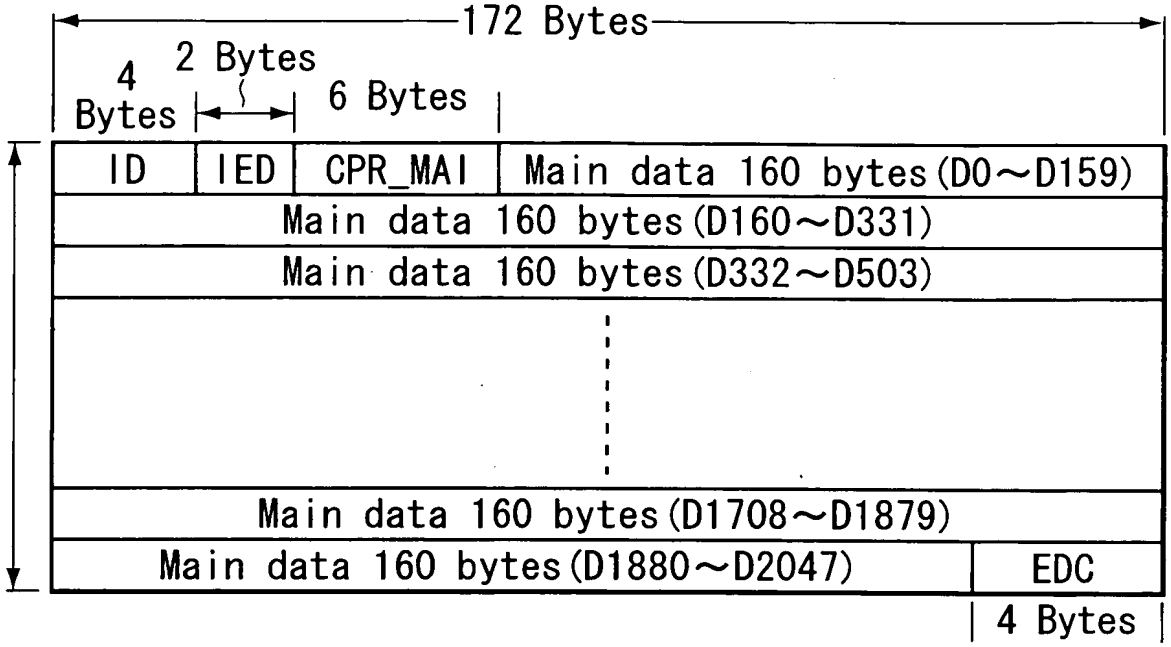
172 Bytes
4 Bytes
2 Bytes
6 Bytes
ID
IED
CPR_MAI
Main data 160 bytes (D0~D159)
Main data 160 bytes (D160~D331)
Main data 160 bytes (D332~D503)
Main data 160 bytes (D1708~D1879)
Main data 160 bytes (D1880~D2047)
EDC
4 Bytes

FIG. 5

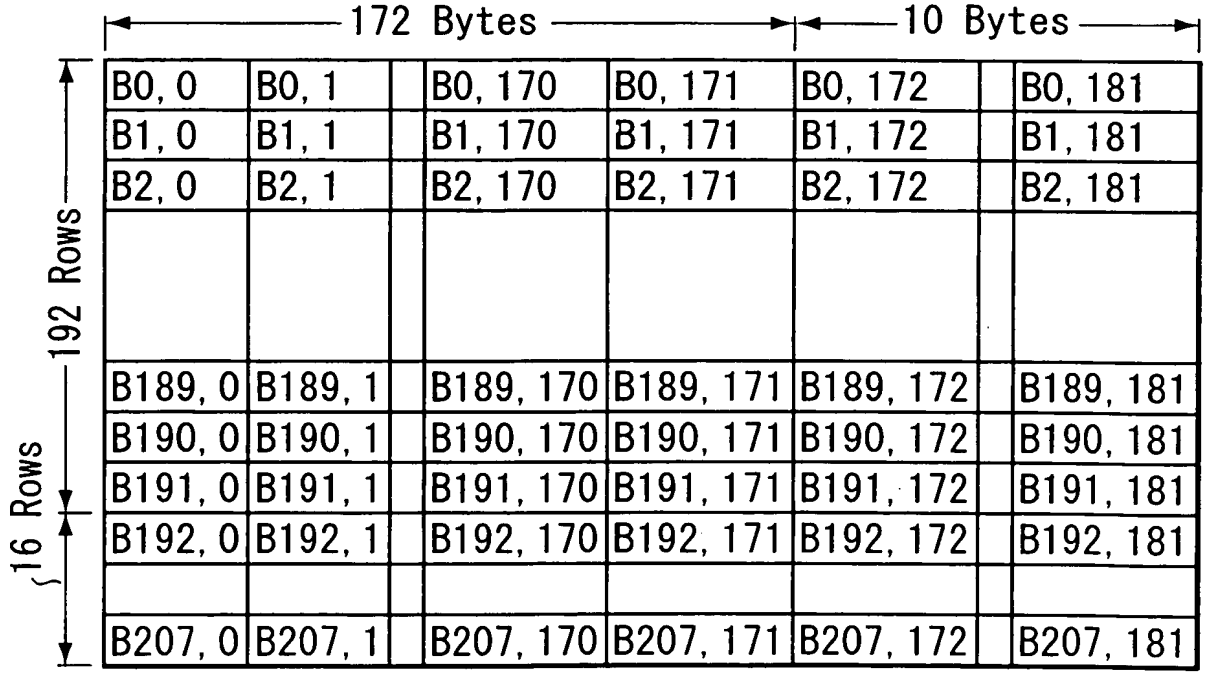
172 Bytes
10 Bytes
192 Rows
16 Rows
B0, 0 B0, 1 B0, 170 B0, 171 B0, 172 B0, 181
B1, 0 B1, 1 B1, 170 B1, 171 B1, 172 B1, 181
B2, 0 B2, 1 B2, 170 B2, 171 B2, 172 B2, 181
B189, 0 B189, 1 B189, 170 B189, 171 B189, 172 B189, 181
B190, 0 B190, 1 B190, 170 B190, 171 B190, 172 B190, 181
B191, 0 B191, 1 B191, 170 B191, 171 B191, 172 B191, 181
B192, 0 B192, 1 B192, 170 B192, 171 B192, 172 B192, 181
B207, 0 B207, 1 B207, 170 B207, 171 B207, 172 B207, 181

FIG. 6

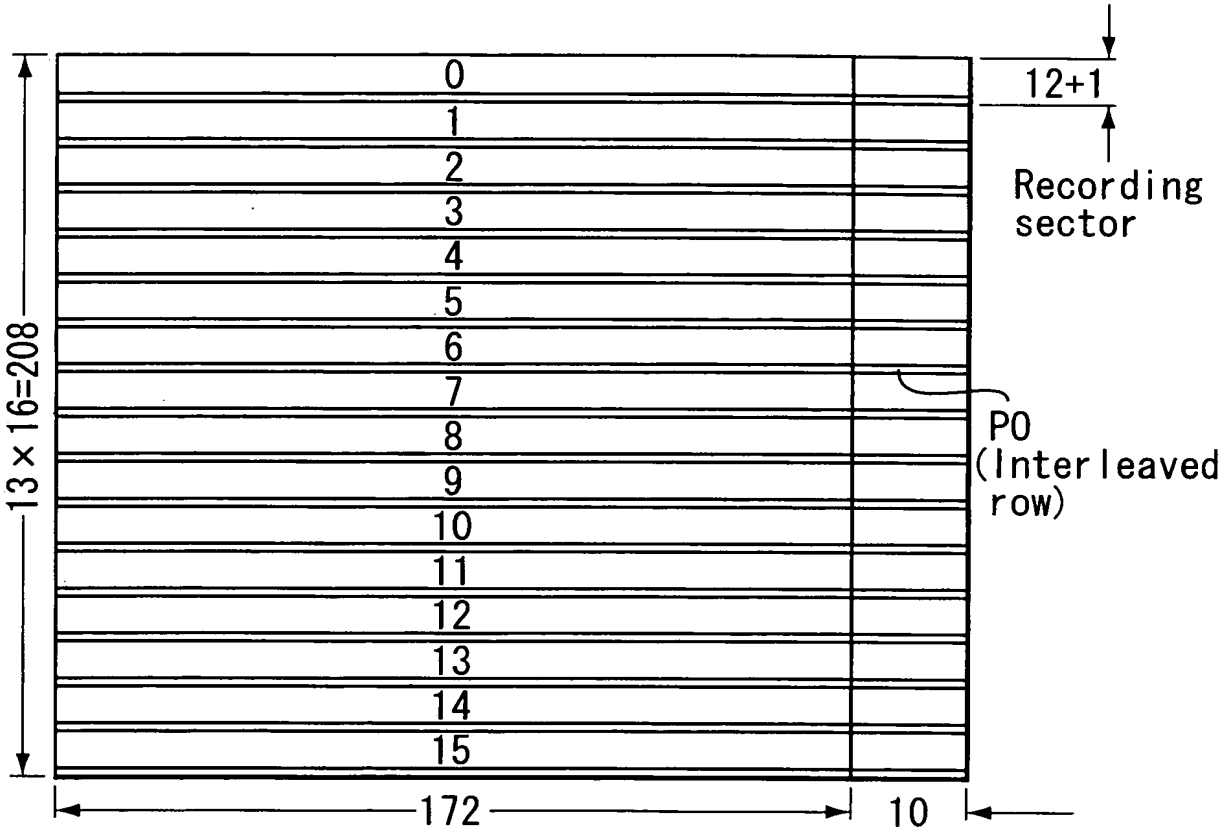
0
1
2
3
4
5
6
7
8
9
10
11
12
13
14
15
13×16=208
172
10
12+1
Recording sector
P0
(Interleaved row)

FIG. 7

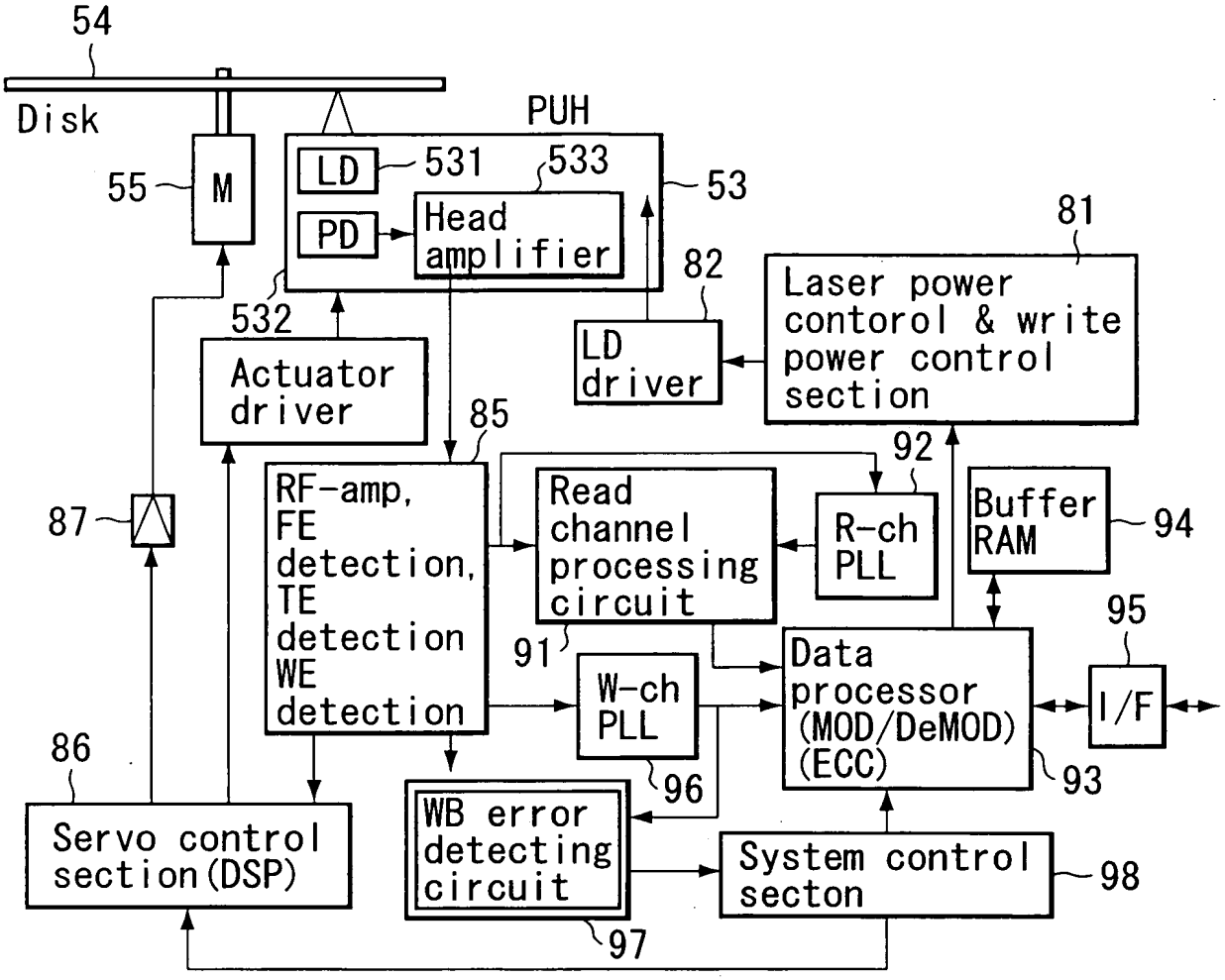
54
Disk
PUH
55
M
LD
531
533
PD
Head amplifier
53
81
82
532
Laser power contorol & write power control section
LD driver
Actuator driver
85
92
87
RF-amp, FE detection, TE detection WE detection
Read channel processing circuit
R-ch PLL
Buffer RAM
94
95
91
W-ch PLL
Data processor (MOD/DeMOD) (ECC)
I/F
86
96
93
WB error detecting circuit
System control secton
98
Servo control section(DSP)
97

FIG. 8

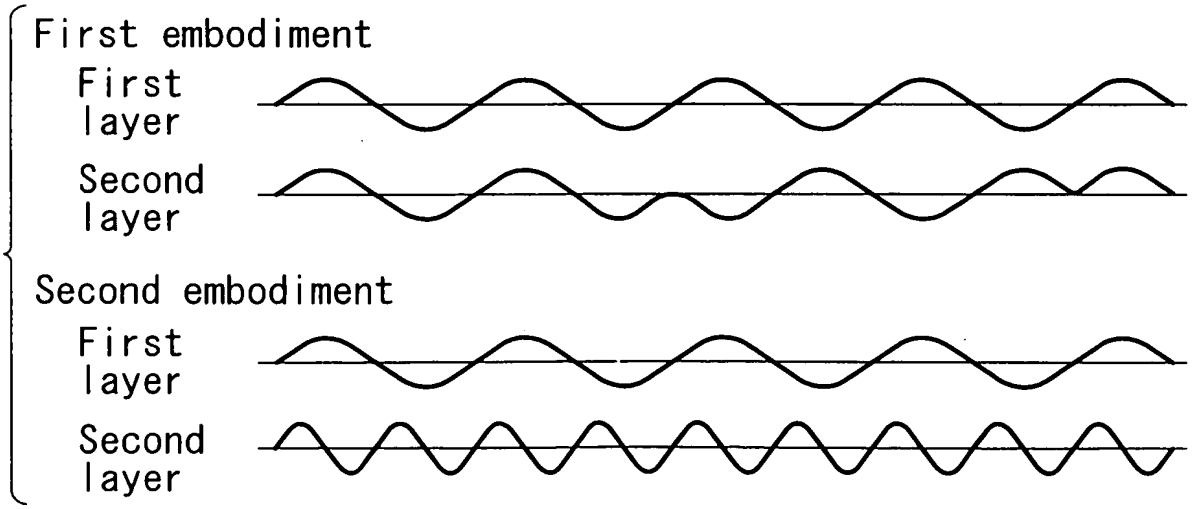
First embodiment
First layer
Second layer
Second embodiment
First layer
Second layer

FIG. 9